Title: VOLTAGE TOLERANT CIRCUIT FOR PROTECTING AN INPUT BUFFER Inventor: Reed et al.



App. No: 10/630,949 Docket No: ARTCP042



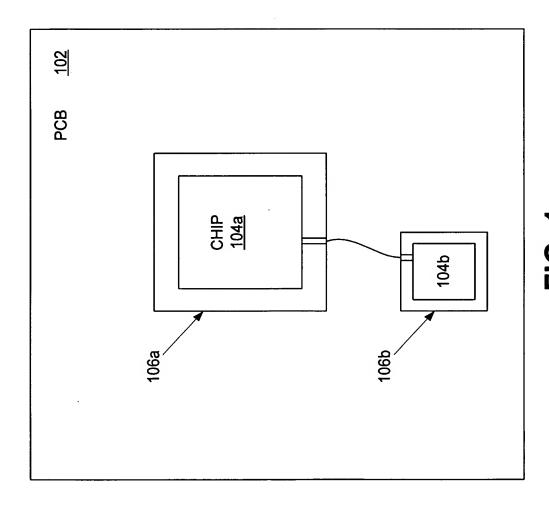


FIG. 1 (Prior Art)

Inventor: Reed et al.

App. No: 10/630,949 Docket No: ARTCP042

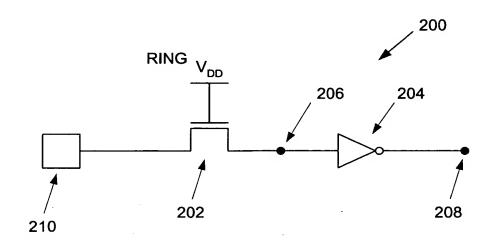
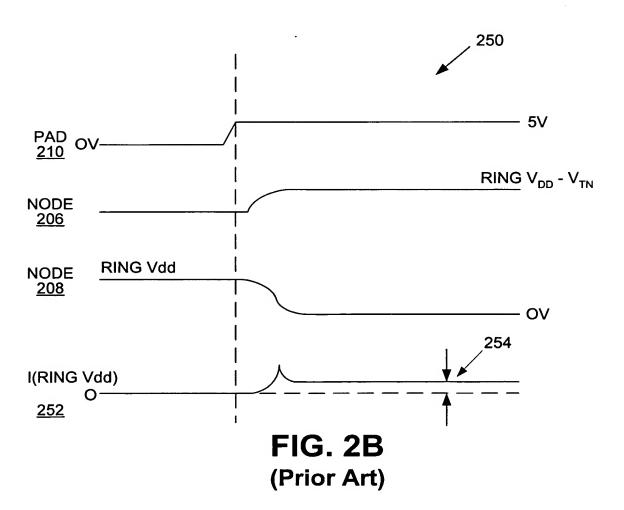
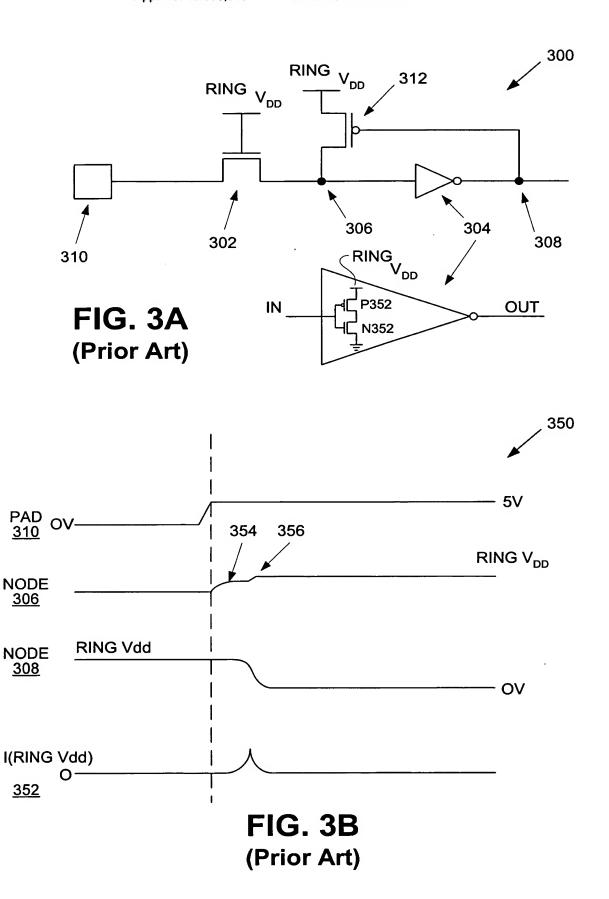


FIG. 2A (Prior Art)



App. No: 10/630,949

Docket No: ARTCP042



App. No: 10/630,949 Docket No: ARTCP042

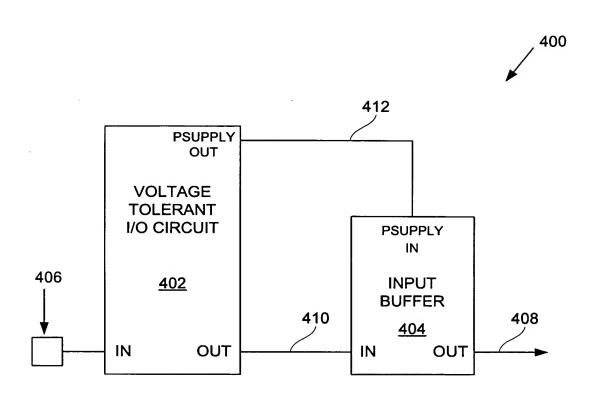


FIG. 4

Inventor: Reed et al.

App. No: 10/630,949 Docket No: ARTCP042

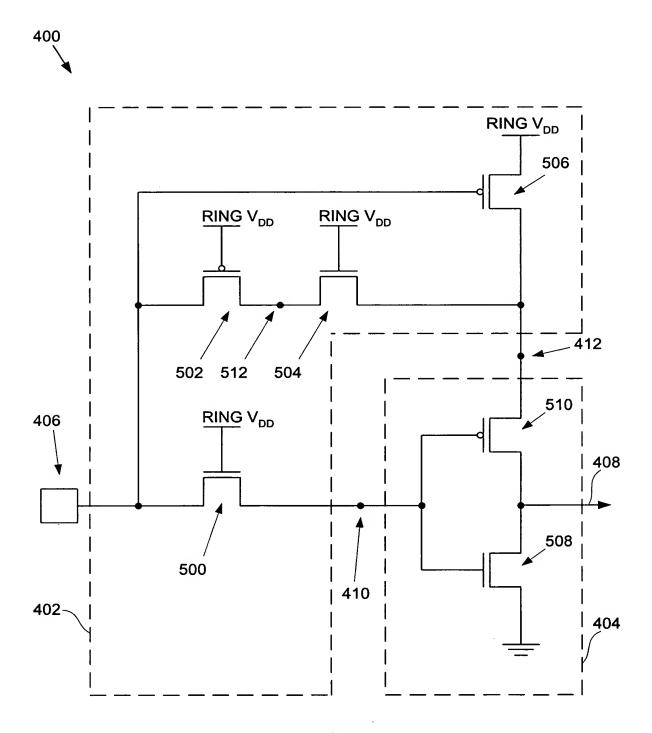


FIG. 5

Inventor: Reed et al.
App. No: 10/630,949 Docket No: ARTCP042

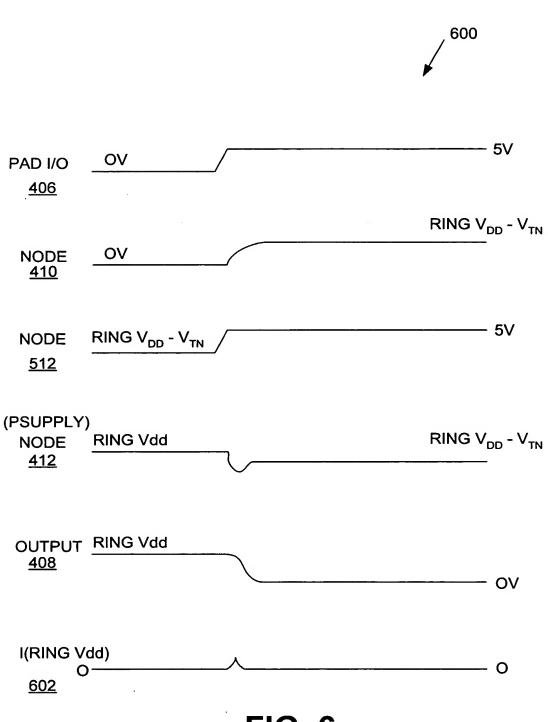
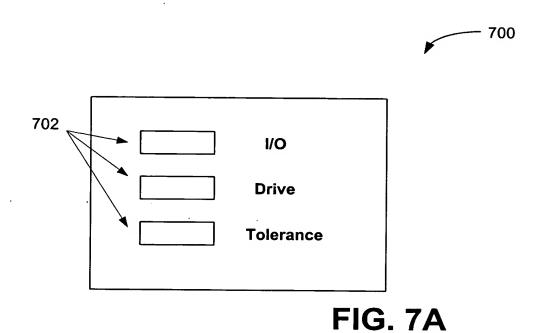


FIG. 6

App. No: 10/630,949

Docket No: ARTCP042



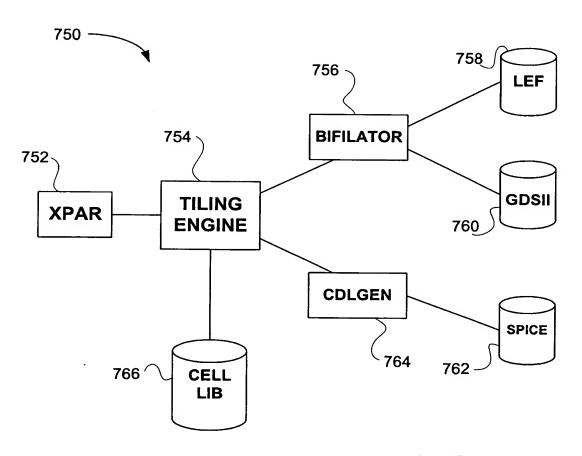


FIG. 7B